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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Toshiya AKAMATSU *et al.*

Confirmation No: 1679

Serial No.: 09/392,722 ✓

Group Art Unit: 2827

Filed: September 9, 1999

Examiner: David E. GRAYBILL

Customer No.: 38834

For: INTEGRATED ELECTRONIC DEVICE HAVING FLIP-CHIP CONNECTION
WITH CIRCUIT BOARD AND FABRICATION METHOD THEREOF

**RESPONSE TO NOTICE OF DRAWING INCONSISTENCY WITH
SPECIFICATION**

Mail Stop: Issue Fee

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 27, 2004

Sir:

In response to the Notice of Drawing Inconsistency With Specification dated May 18, 2004,
please amend the above-identified application as follows: